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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/604,065	06/25/2003	Masuhiko Natsuhara	039.0015	1064

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EXAMINER
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KACKAR, RAM N

ART UNIT	PAPER NUMBER
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1763

MAIL DATE	DELIVERY MODE
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09/05/2007

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

**Office Action Summary**

Application No.

10/604,065

Applicant(s)

NATSUHARA ET AL.

Examiner

Ram N. Kackar

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 30 July 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1,3,5 and 6 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,3,5 and 6 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)                                | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                       | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

**DETAILED ACTION**

***Continued Examination Under 37 CFR 1.114***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 7/30/2007 has been entered.

***Claim Rejections - 35 USC § 112***

2. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

3. Claims 1, 3, 5 and 6 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. In this instance the limitation "electrodes for supplying power to said electrical circuit, said electrodes being separated by an interval that is 10% or more of the thickness of the wafer holder body, and in such a manner" in as far as it is intended to point to a structural limitation, is a new matter.

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

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5. Claims 1, 3,5 and 6 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. In this instance the limitation “electrodes for supplying power to said electrical circuit, said electrodes being separated by an interval that is 10% or more of the thickness of the wafer holder body, and in such a manner” in as far as it is intended to point to a structural limitation, is indefinite. If the applicant believes that this points to a structural limitation it is not clear which that limitation would be. For the purpose of this examination this limitation is not given any patentable weight.

***Claim Rejections - 35 USC § 103***

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. **Claims 1 and 3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kawanabe et al (US 6133557) in view Mogi et al (US 6272002).**

Kawanabe et al disclose a sintered aluminum nitride (Abstract) wafer holder having an electrical circuit inside it (Fig 3A-12) and electrodes to supply power to the circuit (13). The wafer holder is 200mm diameter and 10mm thick (Col 13 lines 53-55). The electrodes supplying power to the circuit appear to be at the corners. The 10% of thickness is 1mm. The spacing between the electrodes therefore would be several times the minimum required distance. Further the aluminum nitride could be up to 99.8% pure (Col 12 lines 34-45) and lack of impurities make

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it superior in corrosion resistance. Impurities of metal like Fe are preferred to be less than 2000 ppm. With slightly less purity it could have oxygen as an oxide sintering aid (Col 12 lines 46-54). The proportion of oxygen with oxide sintering aid of a range of 0.5- 20 wt% could be less than 2%.

The limitation of the temperature uniformity being within  $\pm 1$  percent is process limitation. However Kawanabe et al disclose uniformity of heating due to high thermal conductivity of aluminum nitride, (This kind of high-purity aluminum nitride based sintered body hardly includes grain boundary phases and can be made superior in corrosion resistance. Furthermore, since its thermal conductivity is a high value of 65 W/m.k, quick heating and uniform heating can be attained easily- Col 12 lines 43-45 and Col 18 lines 9-13).

Kawanabe et al do not disclose the material of the electrode supplying power to the heater element.

Mogi et al disclose that the electrodes for supplying power to electrodes could be gold or nickel plated tungsten (Col 10 lines 18-30) and where tungsten electrodes are exposed they are electroplated with nickel and gold.

Therefore it would have been obvious for one of ordinary skill in the art to have power supply electrodes to be made of tungsten and plated with gold or nickel for protection against corrosion or oxidation.

8. **Claims 5-6 are is rejected under 35 U.S.C. 103(a) as being unpatentable over Niori et al (US 5280156) in view of Kawanabe et al (US 6133557) and further in view of Mogi et al (US 6272002).**

Niori et al disclose a wafer holder, which could be of aluminum nitride having an electrical circuit inside it (Fig 8) and electrodes to supply power to the heating circuit (8) and an electrode to supply power to the electrostatic chuck (7A). The electrodes supplying power to the heating circuit appear to be at the periphery (8) and to the chuck at the center. The 10% of thickness would typically be 1-2 mm. The spacing between the electrodes therefore (typically 75-100 mm) would be several times the minimum required distance. The material of the wire 8 is disclosed to be tungsten.

Niori et al do not disclose the purity of aluminum nitride wafer holder.

Kawanabe et al disclose a sintered aluminum nitride (Abstract) wafer holder having an electrical circuit inside it (Fig 3A-12) and electrodes to supply power to the circuit (13). Further the aluminum nitride could be up to 99.8% pure (Col 12 lines 34-45) and lack of impurities make it superior in corrosion resistance. Impurities of metal like Fe are preferred to be less than 2000 ppm. With slightly less purity it could have oxygen as an oxide sintering aid (Col 12 lines 46-54). The proportion of oxygen with oxide sintering aid of a range of 0.5- 20 wt% could be less than 2%.

Therefore it would have been obvious for one of ordinary skill in the art to have a highly pure sintered aluminum nitride wafer holder for its corrosion resistance and oxygen for sintering aid.

Niori et al in view of Kawanabe et al do not disclose the material of the electrode supplying power to the heater element.

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Mogi et al disclose that the electrodes for supplying power to electrodes could be gold or nickel plated tungsten (Col 10 lines 18-30) and where tungsten electrodes are exposed they are electroplated with nickel and gold.

Therefore it would have been obvious for one of ordinary skill in the art to have power supply electrodes to be made of tungsten and plated with gold or nickel for protection against corrosion or oxidation.

### ***Response to Arguments***

Applicant's arguments filed 7/30/2007 have been fully considered but they are not persuasive.

Applicant's continued arguments regarding uniformity of  $\pm 1$  percent are not noted. Further to the response in the last office action it is noted that the applicant has not provided any persuasive argument to treat this limitation any thing other than a process limitation.


### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ram N. Kackar whose telephone number is 571 272 1436. The examiner can normally be reached on M-F 8:00 A.M to 5:P.M.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Parviz Hassanzadeh can be reached on 571 272 1435. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Ram Kackar

Primary Examiner AU 1763